WEST Search History

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DATE: Monday, June 28, 2004

Hide?	Set Name	Query	Hit Count
		DB=EPAB; PLUR=YES; OP=ADJ	
☐	L16	memory and area adj3 (8F$)	0
☐	L15	cell and area adj3 (8F$)	0
		DB=PGPB,USPT; PLUR=YES; OP=ADJ	
☐	L14	L11 and area adj3 (8F$)	5
☐	L13	L12	0
☐	L12	L11 and area ajd (8F$)	0
☐	L11	L10 and load	297
☐	L10	L9 and power supply	469
☐	L9	L8 and transistor	765
☐	L8	L7 and PMOS	766
☐	L7	L6 and NMOS	984
☐	L6	L3 and switching adj1 (transistor or device)	4321
☐	L5	L3 and switching ajd1 (transistor or device)	0
☐	L4	L3 and switching device	1601
☐	L3	memory cell	58289
☐	L2	memory cell	58289
☐	L1	memory	624736

END OF SEARCH HISTORY

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Primary Examiner